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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,613	04/14/2004		William W. Wiles JR.	88664 00001	5086
20873	7590	09/14/2005		EXAM	INER
LOCKE LII	DDELL &	& SAPP LLP	NGUYEN, HIEU P		
ATTN: SUE	COTT ·				
2200 ROSS A	AVENUE		ART UNIT	PAPER NUMBER	
SUITE 2200			2817		
DALLAS, T	X 75201	-6776		_	

DATE MAILED: 09/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		H1
	Application No.	Applicant(s)
	10/823,613	WILES ET AL.
Office Action Summary	Examiner	Art Unit
	Hieu Nguyen	2817
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	correspondence address
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1: after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ti y within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS fron , cause the application to become ABANDON	mely filed ys will be considered timely. n the mailing date of this communication. ED (35 U.S.C. § 133).
Status		
Responsive to communication(s) filed on <u>04/14</u> This action is FINAL . 2b) ☐ This Since this application is in condition for alloware closed in accordance with the practice under E	action is non-final. nce except for formal matters, pr	
Disposition of Claims		
4) Claim(s) 1-4 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) Claim(s) 1-4 is/are allowed. 6) Claim(s) is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or		
Application Papers		
9) The specification is objected to by the Examine 10) The drawing(s) filed on 14 April 2004 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example 11.	⊠ accepted or b) objected to drawing(s) be held in abeyance. So tion is required if the drawing(s) is o	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burear * See the attached detailed Office action for a list	ts have been received. ts have been received in Applica rity documents have been receiv u (PCT Rule 17.2(a)).	tion No ved in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail I 5) Notice of Informal 6) Other:	

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DETAILED ACTION

Specification

The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 CFR 1.75(d)(1) and MPEP § 608.01(o). Correction of the following is required:

Regarding claim 1, the specification fails to support the transimpedance amplifier having "a first voltage divider circuit coupled between said first amplifier output and said first amplifier non-inverting input" and "a second voltage divider circuit coupled between said first amplifier output and said second MOS resistor device source terminal", in combination with the rest of the limitations of the claim(s).

Regarding claim 2, the specification fails to support the transimpedance amplifier having "a photodiode coupled between said first amplifier non-inverting input and said inverting input", in combination with the rest of the limitations of the claim(s).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Hadley (U.S 5786730) discloses a transimpedance amplifier comprising: an amplifier (2), MOS resistor (8) and a voltage divider (potential divider defined by resistors 5 and 6), but lacks the second amplifier coupled to the gate terminal of MOS resistor device.

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Yoon (U.S 6593810) discloses a transimpedance amplifier system comprising: a first gain stage (see detail of Fig 4) having a single input and an output, an NMOS resistor (15), a second amplifier (55) having an inverting input, a non-inverting input and an output being coupled to gate terminals of the NMOS resistor, a voltage divider (see Fig. 4, potential divider defined by resistors R1 and R2), but the first amplifier doesn't have two input terminals for inverting and non-inverting inputs.

This application is in condition for allowance except for the following formal matters: failing to provide proper antecedent basis for the claimed subject matter (see claims 1 and 2).

Prosecution on the merits is closed in accordance with the practice under Exparte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO**MONTHS from the mailing date of this letter.

Reasons for allowance

Claims 1 and 2 would be allowable if amended to overcome the objections above.

The following is an examiner's statement for reasons for allowance:

Claims 1-4 are allowed over the prior art because none of the prior art disclosed or suggested showing the particular structure recited in these claims namely:

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Claims 1-4 call for, among others, a structure of a transimpedance amplifier comprising "a first amplifier having an inverting input, a non-inverting input and an output" and "a second amplifier having an inverting input, a non-inverting input and an output." In combination with the rest of the limitations of the claim(s).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hieu Nguyen whose telephone number is 571-272-0218. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

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Hieu Nguyen AU: 2817

rimary Examiner

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